## 在CUDA中实现奇异 值分解算法的提速

答辩人:

指导教师: 阮吉寿

赵佳









## 奇异值分解应用领域



























Control M1 2hr M1 12hr LTR6 2hr Cell type M1 LTR6 M1 LTR6 M1 LTR6 Time (hr) 12 2 6 9 12 2 6 9 12 12 2 6 9 12 A \_ / T





## 奇异值分解在实际应用中的瓶颈

#### 即便是目前最快的分解算法,

# 算法复杂度 $O(m^*n^2) + O(m^2*n)$

 $A_{m \times m \times \times} = U_m \sum_n U_n V_n^T$ 

CPU与GPU浮点运算能力比较





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3.06 GHz, Myrinet

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## CUDA架构下GPU与CPU的差异



DRAM

CUDA架构下GPU



DRAM



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| Page 1 of 1 Go  | Sort by: Publication Date  |
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| Author(s): Lahabar, S; Narayanan,<br>Conference Information: 23rd IEEE<br>Symposium, Date: MAY 23-29, 200   | PJ<br>E International Parallel and Distributed Processing<br>09 Rome ITALY   |
| PROCESSING, VOLS 1-5 Pages<br>Times Cited: 0  | ition on GPU using CUDA  |
| Author(s): Lahabar, S.; Narayanan,<br>Conference Information: 2009 IEEE<br>Processing (IPDPS), Date: Rome<br>Source: 2009 IEEE International S<br>Pages: 10 pp. Published: 2009 | , P.J.<br>E International Symposium on Parallel & Distributed<br>Italy<br>Symposium on Parallel & Distributed Processing (IPDPS)   |
|   | t a Database Web of Science<br>t a Database Web of Science<br>Page 1 of 1 G<br>Print E-mail Add to Mark<br>Save to EndNote, RefMan, Proc<br>1 Title: Singular Value Decompose<br>Author(s): Lahabar, S; Narayanan,<br>Conference Information: 23rd IEEE<br>Symposium, Date: MAY 23-29, 20<br>Source: 2009 IEEE INTERNATION<br>PROCESSING, VOLS 1-5 Pages<br>Times Cited: 0<br>2 Title: Singular value decompose<br>Author(s): Lahabar, S; Narayanan<br>Conference Information: 2009 IEEE<br>Processing (IPDPS), Date: Rome<br>Source: 2009 IEEE International S<br>Pages: 10 pp. Published: 2009 |





- 2008年 张舒实现基于CUDA架构512×512规模的SVD算法。
- 2009年Sheetal Lahabar 基于CUBLAS实现了QR-SVD算法。
- 2009年EM Photonics, Inc发布CULA1.0,比MKL加速3.2倍。
- 2009年赵佳百项工程实现了CUDA架构下SVD,比MKL加速3.5倍。











单边雅可比算法采用逐次平面旋转的方法求W与V。设A=A。逐次旋转方

阵R, 使A, 的某两列正交化, 反复进行旋转变换:。

$$A_{k+1} = A_k R_k \ (k = 0, 1, \cdots)$$

可得到矩阵序列<sup>{A<sub>k</sub></sub>}。适当选择列的正交化顺序,则<sup>A<sub>k</sub></sup>的各列趋于两两正交,即<sup>A<sub>k</sub></sup>趋向于W,而  $V = R_0 R_1 \dots R_s$ ,这里s是旋转的总次数。。</sup>

设 $R_k$ 使p、q两列正交,则 $A_k$ 与 $A_{k+1}$ 仅在p、q两列不同。有:。

$$\begin{cases} a_p^{(k+1)} = a_p^{(k)} \cos \theta - a_q^{(k)} \sin \theta \\ a_q^{(k+1)} = a_p^{(k)} \sin \theta - a_q^{(k)} \cos \theta \end{cases}$$



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## Jacobi-SVD列调度方案:round-robin

以m=8为例说明列对构造过程:



















# Jacobi-SVD实现细节

## 合理利用零号线程

在实现过程中,主要通过参数传递方式确定旋转的次数,然后每个 Block 中第一个线程计算数

对:

8

```
if( blockIdx.x == 0 && threadIdx.x == 0)
{
    calculate the row pairs;
    }
    if( blockIdx.x != 0 && threadIdx.x == 0)
    {
    calculate the row pairs;
    calculate the row pairs;
```





## **Jacobi-SVD实现细节** 合理利用零号线程 If (threadIdx.x == 0) { $c = \frac{1}{\sqrt{1+t^2}}, s = tc.$

$$t = \frac{sign(\tau)}{|\tau| + \sqrt{\tau^2 + 1}}$$

$$\tau = \frac{a_j^T a_j - a_i^T a_i}{2a_i^T a_j}$$





# Jacobi-SVD实现细节 Blocks And Threads

●虽然处理器规模小于n/2, Block被分配到SM内执行对 用户透明,只需分配n/2个Block。

dim3 block\_num (Row>>1, 1, 1); //Row stands for the row of the matrix

●将矩阵转置,将列队转换成行对。

●权衡每个Block共享16K的share memory等诸多限制,

选取Block size为1×256。

**\*** *dim3 thread\_num* (256, 1, 1);



## Jacobi-SVD实现细节

## 归并算法

### reduce bank-conflict and waiting time

 $\bullet if(THREADS_NUM >= 512) \{ if (tid < 256) \{ value1[tid] += value1[tid + 256]; \} \_syncthreads(); \} \\ \label{eq:synchreads}$ 

• if(THREADS\_NUM>=256) { if (tid < 128) { value1[tid] += value1[tid + 128];} \_\_syncthreads();} \

- if(THREADS\_NUM>=128) { if (tid < 64) { value1[tid] += value1[tid + 64];} \_\_syncthreads();} \
- if(THREADS\_NUM>=64) { if (tid < 32) { value1[tid] += value1[tid + 32]; \

value1[tid] +=value1[tid + 16]; \

- value1[tid] += value1[tid + 8]; \
- value1[tid] += value1[tid + 4]; \
- value1[tid] += value1[tid + 2]; \
- value1[tid] += value1[tid + 1]; } }







## 测试平台简介

#### 显卡处理单元(Graphic Processing Unit, GPU)

- •Tesla T10,显存4G
- •单卡浮点处理峰值为1 Tflops
- •双精度浮点处理能力为87 Gflops

#### MKL环境

- •Intel Core 2 Duo CPU E6750@2.66GHz
- •浮点运算能力22.4Gflops

#### Matlab环境

•Matlab版本为: Version 7.8.0.347 (R2009a) 64-bit

8核 lotel(R) Xeon(R) CPU E5405@2.00GHz处理器







▶本文先通过随机生成0-100内随机矩阵,反复测试12次取平均值,去掉最大值、最小值,取剩余10次的平均值即为所得结果。避免由于某次时间过好或者过坏影响测试结果。

▶所有实验数据均采用单精度浮点运算数(single-precision), IEEE32位 浮点数值的形式。

▶由于GPU的内存带宽可高达141GB/s,与单边Jacobi迭代消耗的时间相比可忽略不计,因此,下文中所测得T1070处理SVD的时间不包括对待处理矩阵拷贝至显存以及所得结果拷贝至内存时间。对矩阵的按列范数预处理在GPU中进行,时间计算在内。







#### 相对于Matlab加速比= Matlab 执行时间 GPU执行时间

# 相对于MKL加速比 = $\frac{MKL$ 执行时间 GPU执行时间

#### 误差 = |计算值 - 精确值| 精确值 \* 100



| X | 优       | 化算法                 | 与Ma    | atlab 🖟   | 人及MKL      | 比较      |
|---|---------|---------------------|--------|-----------|------------|---------|
|   | length  | matlab (s)          | MKL(s) | T1070 (s) | 加速(Matlab) | 加速(MKL) |
|   | 512*512 | 3.60                | 0.58   | 0.76      | 4.74       | 0.77    |
|   | 1k*1k   | <mark>33.0</mark> 6 | 11.26  | 1.96      | 16.90      | 5.75    |
|   | 2k*2k   | 797.51              | 114.63 | 17.78     | 44.86      | 6.45    |
|   | 3k*3k   | 3262.65             | 402.70 | 52.83     | 61.76      | 7.62    |
| * | 4k*4k   | 7770.88             | 898.23 | 124.13    | 62.60      | 7.24    |

#### T1070、Matlab、MKL求解SVD速度对比



### 加速(Matlab)



#### 加速(MKL)



TANJIN

## 优化算法与Lahabar 文献比较

| Re                  | length | GTX280(Lahabar)(s) | C1060(mine)(s) | length  | 加速比◎>                |
|---------------------|--------|--------------------|----------------|---------|----------------------|
| Resi                | 8k*2k  | 111.3              | 128.847        | 8k*2048 | 0.86 <sup>rate</sup> |
| Ref<br>Searc        | 8k*1k  | 50.364             | 33.150         | 8k*1024 | 1.52                 |
| r (<br>Fisc         | 8k*512 | 26.33              | 5.014          | 8k*512  | 5.25                 |
| <b>Г</b> С(<br>Г M2 | 8K*256 | 13.96              | 1.354          | 8K*256  | 10.31                |
| mon                 | 8k*64  | 5.016              | 0.262          | 8k*64   | 19.12                |
|                     | 8k*32  | 3.506              | 0.136          | 8k*32   | 25.71                |

优化算法与Lahabar 文献比较





## 优化算法误差分析









奇异值误差分布条形图







#### ▶在CUDA环境中实现了Jacobi-SVD算法。

>当处理的矩阵规模达到4096\*4096时,得到相对于
Matlab了60倍以上的提速,相对于MKL7倍的加速。
>通过对实验数据分析,当处理长宽比例大于16时,算法
加速明显。





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| Photo album                   | Email: benbenwo1091@163.com   |
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| Usefull Links                 | Singular Value Decomposition (SVD) is wildly used and an important tool for reducing rank. The fastest method dealing with SVD of matrix has the complexity of , which affects the speed of SVD and the scale dealt with. In this paper, by using the powerful capacity of floating point operation in GPU, we have implemented one-sided Jacobi based SVD in CUDA, which could deal with Large scale dense matrix. When dealing with the matrix larger than , the program in this paper is 60 times faster than the program in Matlab, and 7 times faster than MKL, which is distributed by Inter Company. The program in CUDA has improve the speed and scale of the problems dealing with SVD. |
|                               | Keyword : Singular Value Decomposition (SVD); one-sided Jacobi Rotation; CUDA; GPU  |
|                               |   |

# ▶英语很重要 ▶可做的东西还是很多

## 基于Multi-GPU的SVD算法。

#### 基于GPU的Tensor-SVD算法。









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## **Thank You!**

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